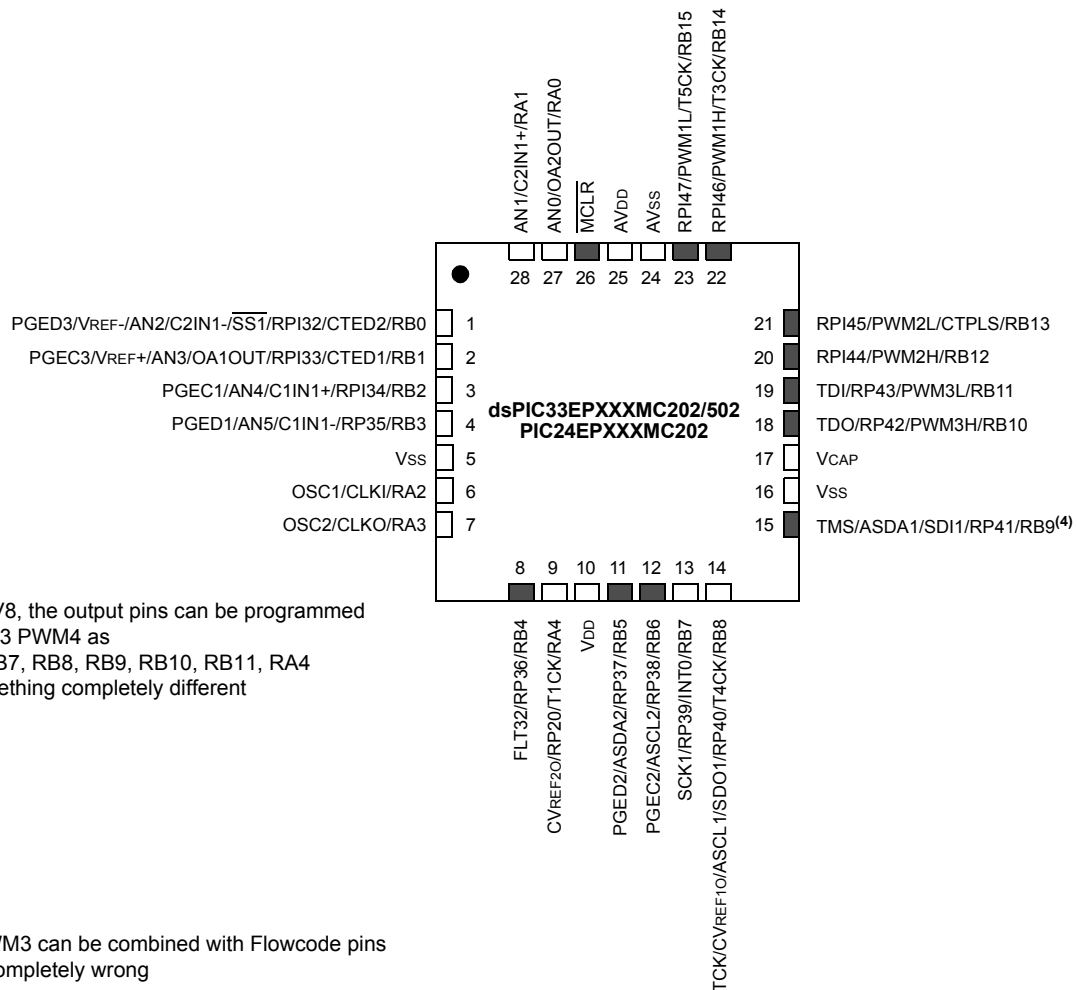


Pin Diagrams (Continued)

28-Pin QFN-S^(1,2,3)

■ = Pins are up to 5V tolerant



In the flowcode V6, V7, V8, the output pins can be programmed for PWM1, PWM2, PWM3 PWM4 as RB3, RB4, RB5, RB6, RB7, RB8, RB9, RB10, RB11, RA4. The diagram shows something completely different. PWM1H = RB14, PWM1L = RB15.

PWM2H = rb12
 PWM2L = rb13

PWM3H = RB10
 PWM3L = RB11

As you can see, only PWM3 can be combined with Flowcode pins. PWM1 and PWM2 are completely wrong.

The DsPIC library is completely wrong.

- Note**
- 1: The RPN/RPIn pins can be used by any remappable peripheral with some limitation. See [Section 11.4 "Peripheral Pin Select \(PPS\)"](#) for available peripherals and for information on limitations.
 - 2: Every I/O port pin (RAX-RGx) can be used as a Change Notification pin (CNAx-CNGx). See [Section 11.0 "I/O Ports"](#) for more information.
 - 3: The metal pad at the bottom of the device is not connected to any pins and is recommended to be connected to VSS externally.
 - 4: There is an internal pull-up resistor connected to the TMS pin when the JTAG interface is active. See the JTAGEN bit field in [Table 27-2](#).